

**AMENDMENTS TO THE CLAIMS**

1. (Currently Amended) An apparatus for detecting flaws in a wafer comprising:

a detection platform holding a wafer thereon for detecting;

a cross-bar ultrasonic detection device positioned above said detection platform for emitting and receiving an ultrasonic wave reflected by the wafer, a width of said ultrasonic detection device being wider than or equal to a radius of said wafer; and

a microprocessor for processing said reflected ultrasonic and transmitting to a monitor; whereby

detecting flaws in said wafer.

2. (Currently Amended) ~~An~~The apparatus for detecting flaws in a wafer according to claim 1, wherein said detection platform is a robot arm for holding and drawing said wafer.

3. (Currently Amended) ~~An~~The apparatus for detecting flaws in a wafer according to claim 1, wherein said detection platform is a chamber-module detection platform having a pad for carrying said wafer, and a table for carrying said pad.

4. (Currently Amended) ~~An~~The apparatus for detecting wafer flaw according to claim 3, wherein said pad is formed with a pair of guiding tracks for guiding said ultrasonic detection device.

5. (Currently Amended) ~~An~~The apparatus for detecting flaws in a wafer according to claim 1, wherein said ultrasonic detection device has a transducer positioned above said detection platform, and a pair of supporting portion connected with two ends of the transducer, said transducer having an emitting portion and a receiving portion mounted therein.

6. (Currently Amended) ~~An~~The apparatus for detecting flaws in a wafer according to claim 5, further comprising a sensor mounted in the transducer or the supporting portions for sensing an incoming and outgoing of said wafer and transmitting a ~~beginning~~beginning or ~~an~~-end message to said microprocessor.

7. (Currently Amended) ~~An~~The apparatus for detecting flaws in a wafer according to claim 1, wherein frequencies of said ultrasonic wave emitted by said ultrasonic detection device are between one hundred million and five thousands million hertz.

8. (Cancelled)

9. (Currently Amended) A method for detecting flaws in a wafer comprising the steps of:

providing a detection apparatus which comprises a detection platform for holding a wafer thereon, a cross-bar ultrasonic detection device positioned above said detection platform, and a microprocessor, a width of said ultrasonic detection device being wider than or equal to a radius of said wafer;

emitting an ultrasonic wave toward a surface of said wafer and receiving a reflected wave from a bottom or a flaw in said wafer;

transmitting said reflected ultrasonic wave to said microprocessor and processing said reflected ultrasonic wave;

determining if said wafer has any flaw for marking the flawed wafer via said microprocessor; and

providing a sensor for inspecting if said wafer is transferred to an end thereof for controlling a detecting sequence.

10. (Currently Amended) A The method for detecting flaws in a wafer according to claim 9, further comprising the step of beeping when detecting said wafer has flaw.

11. (Currently Amended) A The method for detecting flaws in a wafer according to claim 9, wherein said cross-bar ultrasonic detection device is positioned above said wafer.

12. (Currently amended) A The method for detecting flaws in a wafer according to claim 9, wherein said ultrasonic detection device has an emitting portion and a receiving portion mounted therein.

13. (New) An apparatus for detecting flaws in a wafer comprising:

a detection platform holding a wafer thereon for detecting;

a cross-bar ultrasonic detection device positioned above said detection platform for emitting an plane ultrasonic wave and receiving the ultrasonic wave reflected from the wafer; and

a microprocessor for processing said reflected ultrasonic and transmitting to a monitor; whereby

detecting flaws in said wafer.

14. (New) The apparatus for detecting flaws in a wafer according to claim 13, wherein said detection platform is a robot arm for holding and drawing said wafer.

15. (New) The apparatus for detecting flaws in a wafer according to claim 13, wherein said detection platform is a chamber-module detection platform having a pad for carrying said wafer, and a table for carrying said pad.

16. (New) The apparatus for detecting wafer flaw according to claim 15, wherein said pad is formed with a pair of guiding tracks for guiding said ultrasonic detection device.

17. (New) The apparatus for detecting flaws in a wafer according to claim 13, wherein said ultrasonic detection device has a transducer positioned above said detection platform, and a pair of supporting portion connected with two ends of the transducer, said transducer having an emitting portion and a receiving portion mounted therein.

18. (New) The apparatus for detecting flaws in a wafer according to claim 17, further comprising a sensor mounted in the transducer or the

supporting portions for sensing an incoming and outgoing of said wafer and transmitting a beginning or end message to said microprocessor.

19. (New) The apparatus for detecting flaws in a wafer according to claim 13, wherein frequencies of said ultrasonic wave emitted by said ultrasonic detection device are between one hundred million and five thousands million hertz.

20. (New) The apparatus for detecting flaws in a wafer according to claim 13, wherein a width of said ultrasonic detection device is wider than or equal to a radius of said wafer.